Energy Efficient Snubber Networks

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Abstract—conventional snubber networks which are used to remove the stress from semiconductor devices during turning-on and turning-off are used to shape the switching locus to keep it within the Safe Operating Area (SOA) and to reduce the turn-on and turn-off switching losses in the switching devices. Usually a large snubber inductance or capacitance is found to be effective in reducing the dissipation in a resistor. It would be more efficient if, after extracting the switching energy from the semiconductor device, to feed this energy somewhere useful instead of dissipating it as a heat. By doing so, the overall efficiency of the converter would be improved. This is the aim of energy-efficient snubber circuits which is presented in this paper.

Index Terms—snubber circuits, semiconductor devices

I. INTRODUCTION

Even when equipped with proper drive circuitry, real semiconductor switches have a non-zero switching time. During the turn-on and turn-off times, the switches deal with simultaneous voltage and current (current rising and voltage falling during the turn-on and current falling and voltage rising during the turn-off). This results on switching loss in the semiconductor. The switch current and voltage waveforms depend on the load circuit as well as on the characteristics of the switch. One way to remove the stress from the switch during the turn-on and turn-off times is to force either the voltage or the current to be zero during the turn-on or turn-off times. This can be done by adding passive components (L-R) in the turn-on and (C-R) in the turn-off. Both the turn-on inductor and turn-off capacitor divert the energy from the switch during the turn-on and turn-off times and force this energy to be dissipated as a heat a resistor. The L-R and LC circuits are called dissipative snubber networks. The word ‘dissipative’ is used because they do not reduce the switching losses but instead of that, they divert the losses from the delicate/expensive semiconductor switches to a rugged/cheap resistor. In this paper a brief review of the dissipative snubber networks are presented followed by some proposed energy-efficient snubber networks where no resistor is used and the stored energy on the turn-on inductor and turn-off capacitor are recovered and fed back to the source. By doing so, not only the semiconductor switches will be stress-free but also the overall efficiency of the converter, where the switch is used, will increase [1, 2].

II. THE SWITCH CHARACTERISTICS

Fig. 1 shows the current and voltage waveforms during the turn-on and turn-off times. If the turn-on and turn-off times are equal, then the relationship between the current through the switch and the voltage across the switch will be linear (as illustrated in Fig. 1). However, if the turn-on time is larger than the turn-off time (which is usually the case), then the i-v relationship can take a different shape as the one shown in Fig. 2.
The whole idea of the snubber network is either to force the voltage across the switch to go to zero (or near zero) during the turn-on or to divert the switch current during the turn-off. By doing so the power dissipated in the switch will be zero (or close to zero) as illustrated in Fig. 3.

Initially, no current flows in ‘S’ and the small snubber inductance ‘L’. As the current through ‘S’ rises during turn-on, a voltage \( V_L = L \frac{di}{dt} \) is developed across \( L \). Assuming that \( i_S \) increases linearly with \( t \), then the voltage across the inductor during the turn-on time: \( V_L = L \frac{i_S}{t_{on}} \).

If we choose \( L \) so that \( V_L = V \), there will be zero voltage developed across ‘S’ during \( t_{on} \), giving zero turn-on loss in the switching device. Note that there is energy of \( \frac{1}{2}L^2 \) is stored in ‘L’ and must be removed (during \( t_{off} \) time) so that the snubber is reset by the start of the next cycle. Resistor ‘R’ and a diode ‘D’ are used to dissipate this energy as heat. The value of ‘R’ is not critical, but two factors restrict it: The time-constant \( L/R \) must be short enough so that the current can decay sufficiently during \( t_{off} \). Also, immediately after ‘S’ has opened it sees a voltage \( V + IR \) across it. The voltage rating of ‘S’ must therefore be increased to withstand such voltage.

The power dissipated in ‘S’ during \( t_{on} \) is ideally zero. The power dissipated in ‘R’ is simply the stored energy multiplied by the switching frequency (1/T).

\[ P_R = \frac{1}{2}L^2 / (2T) \] (Note that \( P_R \) is independent of ‘R’).

If ‘L’ is smaller than its optimum value, some power will be dissipated in ‘S’ but correspondingly less will be dissipated in ‘R’. The total loss \( P_{loss} = P_{on} + P_R \) remains the same. If ‘L’ is larger than its optimum value, the power dissipated in ‘S’ stays at zero while the dissipation in ‘R’ increases as shown in Fig. 4.

### III. Dissipative Turn-On Snubber Network

The turn-on snubber network is simply a circuit connected in series with the semiconductor switch. The turn-on snubber consists of an inductor ‘L’, a resistor ‘R’ and a diode ‘D’ as shown in Fig. 4. The basic idea is to select an appropriate value of an inductor so that during the turn-on time the voltage developed across ‘L’ equals the supply voltage ‘V’ and hence the voltage across the switch ‘V_s’ becomes zero (or near zero) during the turn-on time.
IV. DISSIPATIVE TURN-OFF SNUBBER NETWORK

The turn-off snubber network is simply a circuit connected in parallel with the semiconductor switch as shown in Fig. 5 in order to divert the current from the switch during the turn-off time. Initially current ‘I’ is flowing in ‘S’ and the small snubber capacitance ‘C’ is uncharged. As the current through ‘S’ decreases, ‘C’ and D provide an alternative path for the balance of I, and the free-wheel inductive load diode remains reverse-biased.

![Fig. 5: Dissipative turn-off snubber network](image)

Assuming that ‘i_s’ falls linearly during t_f then:

\[ i_s(t) = I(1 - \frac{t}{t_f}) \]

Where the time origin has been taken at the start of turn-off. The current flowing into ‘C’ therefore increases linearly according to:

\[ i = \frac{I}{t_f} \]

Putting this into the capacitance equation:

\[ i = C \frac{dv}{dt} \]

The energy dissipated in S during turn-off:

\[ W = \frac{1}{2} \frac{I^2}{C} \int \left( 1 - \frac{t}{t_f} \right) dt = \frac{I^2 t_f^2}{24C} \]

If the switch current reaches zero before the capacitor fully charges, the capacitor voltage ‘V_c’ will be:

\[ V_C = \frac{I t_f}{2C} \]

Solving for the value of ‘C’:

\[ C = \frac{I t_f}{2 V_C} \]

The resistor is chosen such that the capacitor is discharged before the next time the transistor turns off. A time interval of three to five time constants is necessary for capacitor discharge. Assuming five time constants for complete discharge, the on time for the transistor:

\[ t_{on} \geq 5RC \]

\[ R \geq \frac{t_{on}}{5C} \]

Now, there is an energy of (½ CV^2) stored in ‘C’ and must be removed (during t_on). The simplest way to do this is to add a diode ‘D’ and a resistor ‘R’ to the snubber. The voltage across ‘C’ decays exponentially when the switch is closed. The value of R is not critical, but two factors restrict it: The time constant CR must be short enough that V_c can decay to a negligible value during t_on. (3 to 5 time-constants will be sufficient). Immediately after the switch closes, it passes a current of (I+V/R) greater than the snubbed current, I. The current rating of S must therefore be increased relative to the unsnubbed case.

Fig. 6 shows that there are two optimum values of ‘C’ which can be selected. C_{opt(1)} can be selected to minimise the total turn-off loss.

\[ C_{opt(1)} = 0.22 \frac{I t_f}{V} \]

C_{opt(2)} can also be selected to minimise the loss in ‘S’ without increasing the total loss.

\[ C_{opt(2)} = 0.91 \frac{I t_f}{V} \]

![Fig. 6: Determining optimum values of ‘C’](image)

V. COMBINED TURN-ON / TURN-OFF SNUBBER

In this combined snubber network (Fig. 7), The values of ‘C’ and ‘L’ are determined as before. ‘R’ must now be able to dissipate the sum of the turn-on loss (P_{R-on}) and the turn-off loss (P_{R-off}). The value of ‘R’ must now be chosen with care as it forms part of both the turn-on and turn-off snubbers.

![Fig. 7: Dissipative turn-off / turn-off snubber network](image)
VI. ENERGY-EFFICIENT TURN-ON SNUBBER NETWORK

In the previous sections, dissipative snubber circuits are covered where a resistor ‘R’ is used to dissipate the energy which otherwise would have been dissipated in the switch. In this section a proposed circuits are used to divert this energy back to the source and hence increasing the overall efficiency of the converter circuit. Fig. 8 shows the principle of energy-efficient turn-on snubber circuit. It works the same way as the dissipative turn-on snubber circuit as far as capturing the switch energy during the turn-on time and storing it in an inductor. However, this energy is transferred via a secondary mutual inductor and a diode to the main voltage source.

![Fig. 8: Energy-efficient turn-on snubber network](image)

The nonlinearity characteristics of inductor [3-5] are utilized in the energy-efficient snubber network. It is well known that the inductance of a linear inductor may be defined as \( L = \frac{\Phi}{i} \), where \( \Phi \) is the magnetic flux and \( i \) is the current producing it. The stored energy in this case is \( W = \frac{1}{2} Li^2 \). Now, what is the equivalent relation for a nonlinear inductor? \( \Phi \) is no longer linearly related to \( i \), so we can define a term ‘incremental inductance’ \( L(i) \) as the rate of change of \( \Phi \) with \( i \).

\[ L(i) = \frac{d\Phi}{di} \]

We can find the stored energy as:

\[ W(i) = \int_0^i \Phi(i) di = \int_0^i L(i) di \]

If the inductor saturates at \( (I_{sat}, \Phi_{sat}) \), then the energy stored at a current \( i > I_{sat} \) may be expressed as:

\[ W(i) = \int_0^{I_{sat}} \Phi(i) di + \int_{I_{sat}}^{i} \Phi_{sat} di \]

\[ W(i) = \frac{1}{2} L_0 I_{sat}^2 + L_0 I_{sat} (i - I_{sat}) \]

For large value of currents the stored energy is considerably less that that of linear inductance. This may be exploited in the snubber circuit. For example, consider 10 \( \mu \)H snubber inductor carrying full load current of 5 A. The stored energy in this case is \( W = \frac{1}{2} L i^2 = 125 \mu J \). Now assume that a satisfactory snubbing can be obtained by using a non-linear inductor of \( L_0 = 10 \mu H \) which saturates at 1A, the stored energy in that case using the equation in (3) will be 45 \( \mu J \).

Thus the non-linear snubber circuit dissipates much less in comparison with linear snubber circuit.

VII. ENERGY-EFFICIENT TURN-OFF SNUBBER NETWORK

The main idea is to use an auxiliary converter to feed the capacitor-stored energy back to the supply. Figure 9 shows the main idea of the energy-efficient turn-off snubber network.

![Fig. 9: Energy-efficient turn-off snubber network](image)

The same way saturable inductor is utilized in energy-efficient turn-on snubber circuit, a saturable capacitor (with a nonlinear dielectric) can also be utilized in energy-efficient turn-off snubber circuit. The stored energy can be expressed as:

\[ W(v) = \int_0^v q(v) dv = \int_0^v C(v) dv dv \]

Saturable snubber capacitors are at present only experimental devices. They are constructed with ceramic dielectrics [6], and the properties of the dielectric are probably quite variable.

VIII. CONCLUSIONS

Dissipative turn-on and turn-off snubber networks are incorporated in converter circuits to improve the reliability of the switching devices, but they may reduce the overall efficiency of the converter. With energy-efficient snubber networks the loss in the snubbers may be reduced with a consequent increase in efficiency. If a saturable inductor is employed in a turn-on snubber, or a saturable capacitor in a turn-off snubber, the energy stored is less than when linear components are used.

REFERENCES


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